



# APPLICATION NOTE

## Design Considerations for ChromaLit™ Ellipse, Candle, and Dome Remote Phosphor Light Sources

January 11, 2012

**Introduction:** The following document describes the technology and application of three dimensional remote phosphor ChromaLit™ sources. These sources provide a uniform Lambertian radiation patterns when combined with efficient blue (455nm dominant) LEDs at specific color temperatures suited to the application. These color temperatures include warm white through cool white and color rendering indices of 70 to 90.

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## APPLICATIONS

- LED retrofit bulb
- Downlight
- Wall Sconce
- Pendant
- Appliance
- Portable consumer lighting
- Chandeliers and decorative applications

## CHROMALIT REMOTE PHOSPHOR REVIEW

The ChromaLit ellipse, candle and dome are molded products that contain the appropriate phosphor(s) necessary to provide the proper color temperature and color rendering when excited by royal blue LEDs with nominal dominant wavelength of 455nm. The distribution of light for these remote phosphor sources are defined by the shape and lambertian surface radiation characteristics. An external diffuse globe will modify the far field distribution pattern and must be considered for applications with special distribution requirements such as that for A19 bulbs defined by Energy Star.

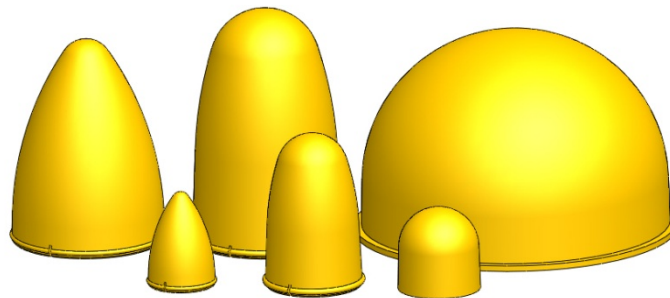


Fig 1. Ellipse, Candle and Dome ChromaLit Sources  
(See ChromaLit data sheet for detailed dimensions).

An example of a light source using the remote phosphor is shown in the graphic below. The blue radiant output from the LED array illuminates the inside surface of the remote phosphor. About half of the incident blue power that is down converted is returned at all angles towards the inside, while the remaining exits the remote phosphor. The LEDs are surrounded by a reflective material, which helps recycle the converted light as well as the blue flux which is unconverted by the phosphor. A portion of the LED blue power is also transmitted through the remote phosphor, which when combined with the converted light, provides the final spectral power distribution. The color temperature, whether warm white such as 2700K or cool white such as 5000K, is defined by the properties of the remote phosphor as are the color rendering properties.

See Intematix Application note “Design Considerations for Remote Phosphor Luminaires and Light Sources” for recommended selection of blue LEDs.

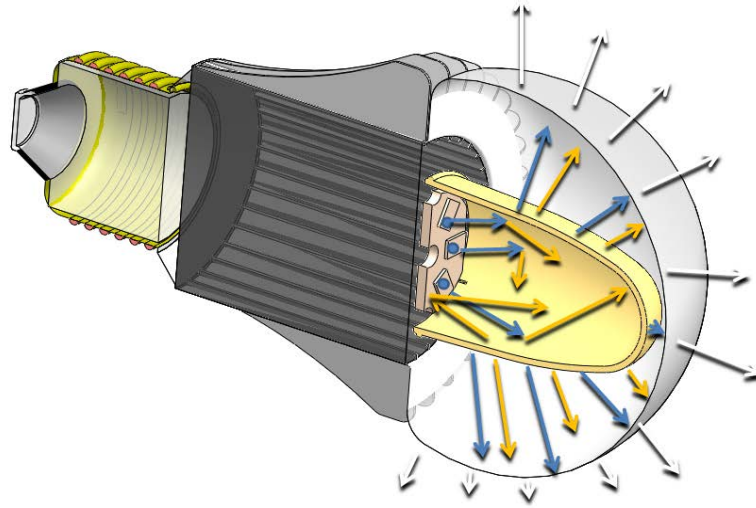


Fig2. Remote phosphor used in A19 bulb (driver not shown).

The higher color temperature sources (4000K and 5000K) have a higher level of transmitted blue flux, and the warmer colors (2700K, 3000K) have significantly less. The warmer colors with high color rendering also have significantly broader red spectral power as shown in the example spectral plots below.

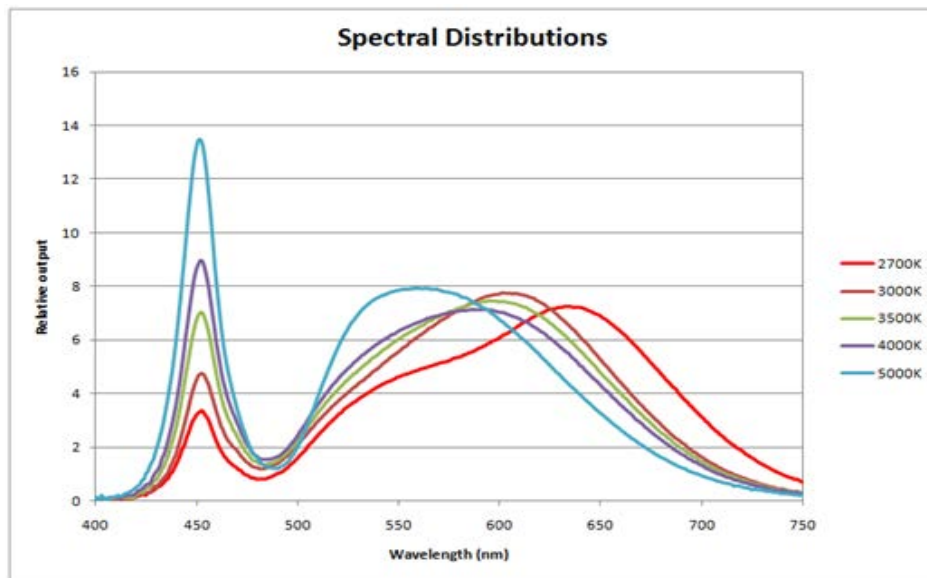


Fig3. Typical spectral power distributions for remote phosphor sources.

Any impact on the spectral characteristics of external optics such as a diffuse outer envelope should be characterized early in the design phase to verify that the desired chromaticity and color rendering properties are maintained. Even if proven to have no impact on the spectral output, an outer envelope will tend to have about 5-10% light loss for limited diffusion levels and 10-15% light loss for heavier diffusion levels.

In the table below source colors are designated CL-xyy, where x is the first digit of CRI and yy are the first digits of color temperature.

Color Designation	CCT <sup>1</sup> (K)	Color Consistency		CRI	Typical Conversion Efficacy (Lm/W <sub>rad</sub> ) at 25°C
		SDCM	CCT (K)		
CL-827	2700	3	±70	>80	165
CL-927	2700	3	±70	>90	140
CL-830	3000	3	±90	>80	185
CL-930	3000	3	±90	>90	150
CL-835	3500	3	±110	>80	190
CL-840	4000	3	±120	>80	195
CL-750	5000	4	±170	>70	220

Table 1. ChromaLit CRI and CCT designations with conversion efficacies.

The conversion efficacy for each ChromaLit design is specified in lumens (Lm) per blue watt (W<sub>rad</sub>) input. This is not to be confused with system efficacy in terms of lumens per electrical input watt. The conversion efficacy provides a starting point in the prediction of how many lumens can be obtained for a given color temperature and CRI knowing the amount of blue LED power presented to the remote phosphor. If the remote phosphor is operated at temperatures beyond the rated maximum, the conversion efficacy will drop slightly. It is therefore important to drive the remote phosphor at or below its rated maximum temperature.

The remote phosphor is designed for LED blue power with dominant wavelength at 455nm. Typical acceptable range of spectral blue power is 450-460nm. However, the color point will vary slightly with average dominant wavelength. For overall system efficacy to be maximized, the LED's efficiency in converting DC electrical power to blue spectral power should be the highest possible. Current state of the art LED blue output power verses DC input power is about 50-56% and is a function of die temperature (drive current). LEDs well below 45% should not be considered for high system luminous efficacy applications or where thermal management is more difficult (when lower LED power is desired).

## THERMAL MANAGEMENT

Maximum rated inside surface temperature for the remote phosphor sources is 110°C. Due to the details of the assembly, it is best to test the temperature at the top, side, and bottom of the part to verify the maximum. For the candle shapes, the temperature is best measured .5 to 1mm below the top center. To achieve the specified conversion efficacy in table 1, it is important that the maximum temperature anywhere in the part not exceed 110°C. Although the temperature of the remote phosphor will increase with blue flux power, the temperature is far more important than the maximum blue flux incident on the remote phosphor with regard to efficacy.

Description	Maximum Values
Maximum operating temperature (T <sub>max</sub> )	110°C
Minimum operating temperature	-40°C
Max storage temperature	110°C
Minimum storage temperature	-40°C

Table 2. Maximum temperatures of ChromaLit sources.

## T<sub>max</sub> LOCATION

For any new design, the maximum inner surface temperature of the ChromaLit must be determined and found to be less than 110°C under fully stabilized, maximum ambient and power up conditions. Since the inner surface is isolated from external free air convection and is continuously exposed to the blue power from the LED when lit, the maximum temperature will be on the inner surface. In some cases, it may be located at an inner surface location near the LED(s) and PCB and depending on orientation, most likely on the topside.

T<sub>max</sub> values should be measured on ChromaLit sources on the inside surfaces at locations illustrated below as a minimum. If a non-contact thermal imaging camera is available, the hot spot can be located on the outside surface and then the inner hot spot can be measured or derived.

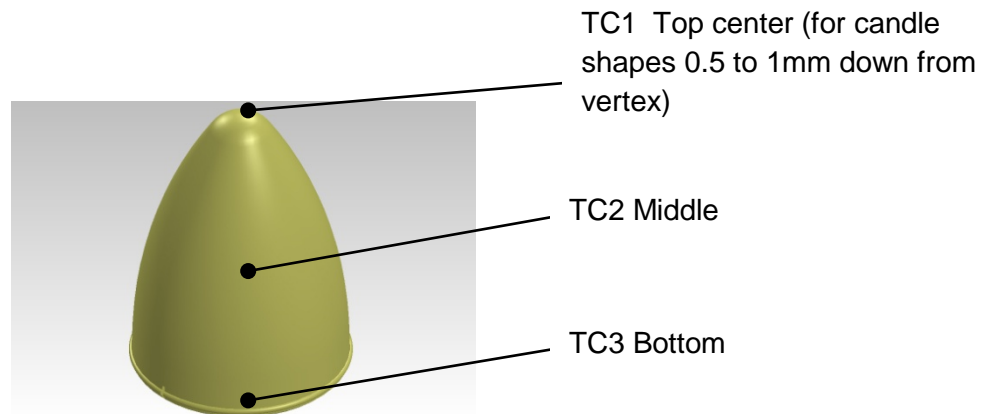


Fig 4. ChromaLit Temperature locations necessary to determine hot spot maximum.

**Inside surface temperature must be determined.**

The maximum temperature for horizontally operating remote phosphor sources will typically be located on the top of the part due to natural convection. Furthermore, the hot spot may also be near the opening due to the proximity of the remote phosphor to the LED and LED PCB. For horizontal orientation and mounting to a high output LED PCB, the hot spot will most likely be as indicated below.

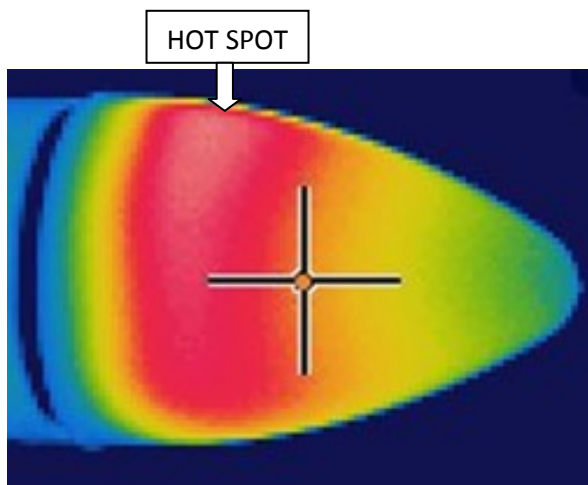


Fig 5a. Remote phosphor hot spot.

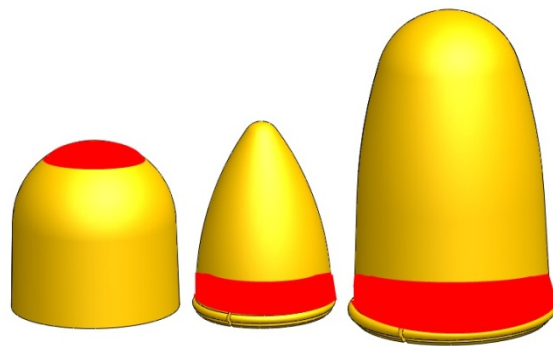


Fig 5b. Typical warmer regions for Ellipse, Candle and Dome

## MEASURING REMOTE PHOSPHOR TEMPERATURES

To measure the surface temperature of the remote phosphor, it is recommended that a 36-gauge insulated K-type thermocouple be used. This has a small weld head to minimize radiation absorption and allows multiple locations to be monitored with minimal impact on light output if that is being monitored as well. Inserting the entire thermocouple inside the remote phosphor is not recommended due to greater inaccuracies in temperature readings due to blue power absorption. This can result in temperature readings about 22 degrees higher than expected.

### Method 1. Through hole drilling.

A small hole can be drilled through the remote phosphor using a Dremel 108 engraving cutter or equivalent and the thermocouple can be fed through the hole from the outside. The weld bead can then be attached on the inside surface away from the entrance hole as shown in the picture below.

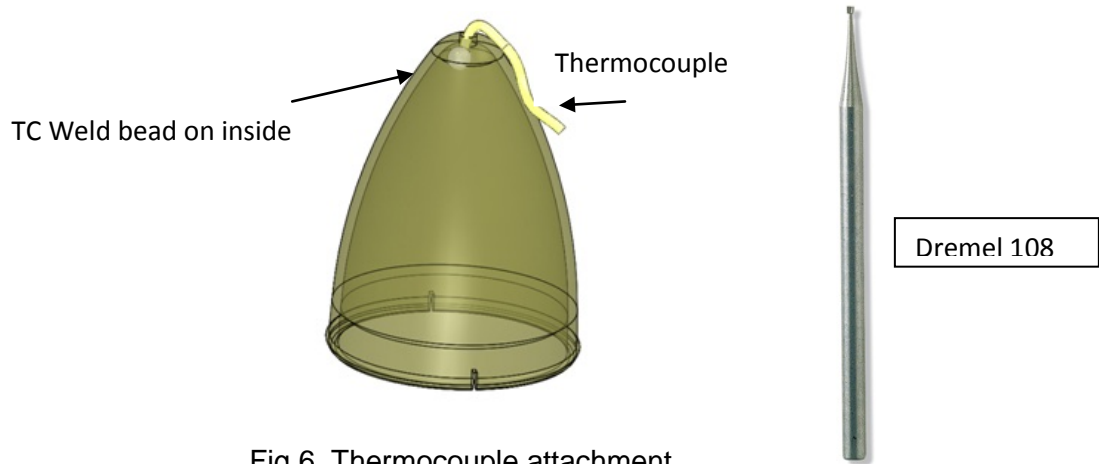


Fig 6. Thermocouple attachment.

Dispense a small amount of 2-part LockTite Poxy Pak on a clean surface as shown on the bottom left. Be sure the two part epoxy is well mixed prior to application.

NOTE: For permanent mounting of the remote phosphor, Intematix recommends 3M 2-part DP-460 high performance epoxy adhesive. It is not currently recommended to use LockTite Poxy Pak for anything other than shorter term engineering temperature characterization. Alternatively, DP-460 can be used as an alternative for temperature test, however, the relative maximum operating temperatures are 149C and 120C for the Poxy Pak and DP-460 respectively.



Fig 7. Dispense 2-part epoxy on clean surface.

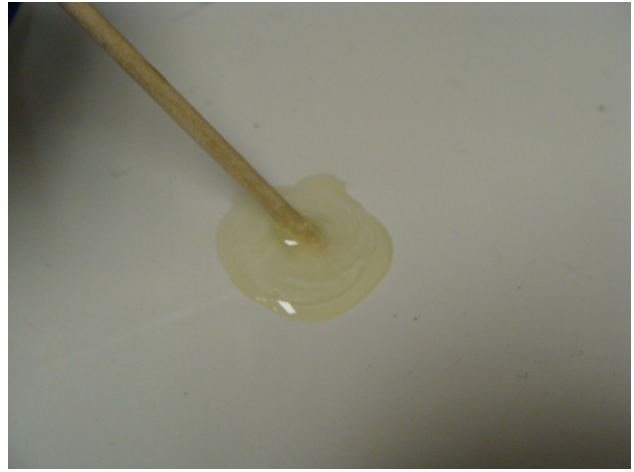


Fig 8. Be sure the epoxy is well mixed.

After the thermocouple is inserted into the remote phosphor hole, it can be taped down on the outside surface. Bending the thermocouple on the inside can be done in such a way as to ensure positive force of the weld bead against the remote phosphor surface prior to applying the cement.



Fig 9. Thermocouple held in place using electrical tape prior to applying epoxy.

Next, drop a small amount (about 1mm diameter) pre-mixed 2-part epoxy onto the thermocouple weld bead on the inside surface. Try to minimize the total amount of epoxy around the weld bead to minimize the thermal mass. After the weld bead is glued into place, a small amount of epoxy can also be applied to the thermocouple wire where it enters the remote phosphor on the outside surface to ensure a tight bond.

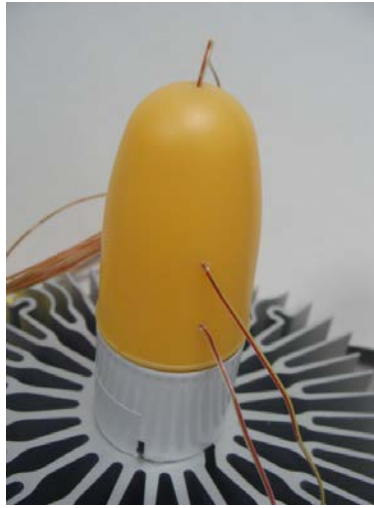


Fig 10. Remote phosphor ready for thermal test. Three locations on inside surface to be monitored.

Allow 90 minutes dry time while maintaining surface contact between the TC and remote phosphor. Use care when moving the assembly even after epoxy is dry since the thermocouple can be pulled away. Taping the thermocouple to provide strain relief during curing is also recommended.

#### Method 2. Partial hole drilling.

A partial hole approximately 0.7 to 0.8mm deep can be made from the outside of the ChromaLit at the three test locations shown in figure 13. The thermocouple is then inserted into the hole and a small amount of adhesive is used to bond the thermocouple to the ChromaLit. Since this is nearly at the inner wall, the temperature reading is a very close match to the case of bonding the probe to the inner surface.

#### Method 3. Thermal imaging camera.

An alternative method of measuring surface temperature is through use of a non contact infrared thermometer. However, to ensure that that the thermometer is not sensitive to the light radiation of the source, an IR thermometer with a detector sensitivity in the 7.5-13 micron region is recommended. All that is required is the emissivity of the material. Calibration to a mounted thermocouple can also be performed to ensure the proper emissivity value is used. Since this technique indicates an outer surface temperature only, it will be necessary to correlate the outside temperature to the inside surface. Note also that if a temperature is required using an external glass or plastic outer envelope, this technique cannot be used since it will determine the outer envelope temperature only.

With the thermocouple techniques described above in option 1 and option 2, thermal stabilization is required. This will typically take at least an hour.

A common method of determining that blue power absorption is not giving false readings is to monitor the steady state temperature immediately after turning of the power to the LEDs. If a large drop in temperature is noted immediately after powering down, this indicates an absorption issue or electronic noise to the thermocouple instrumentation that must be corrected.

## THERMAL TEST EXAMPLE #1 – ENGINEERING PROTOTYPE (OVER DRIVEN)

A destructive test is performed using the warm white part shown below. The ten LED board shown on the left is used to over-power the remote phosphor and monitor top (T1), side (T2), and bottom (T3) temperatures.



Fig 11. 10 LED PCB

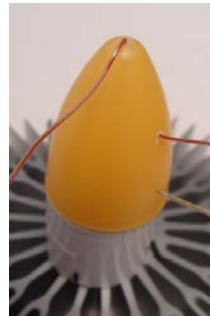


Fig 12. Test assembly

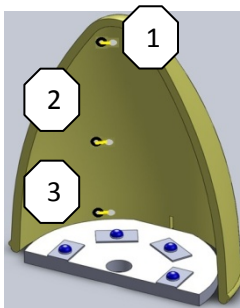


Fig 13. Temperature Test Locations T1 (top), T2 (side), T3 (bottom)

The table below summarizes the data obtained. Note that the lumens are over the recommended 600 lumens for this part throughout the test. The chart below the table shows that the conversion efficacy of the source is nearly constant until about 100°C where a decrease in conversion efficacy (lumens per blue watt input) results.

Thermal Test Summary

Current (I)	Voltage(V)	Power(W)	T1(degC)	T2(degC)	T3(degC)	Temp Comment	Wrad Blue Power (W)	Lumens	lm/Wrad	lm/DC P	Lumens Comment
0.3	27.2	8.16	67	71	69		3.756	680.3	181.1235	83.3701	lumens just over spec
0.4	27.3	10.92	79	88	84		4.837	859.7	177.7341	78.72711	lumens over spec
0.5	27.3	13.65	92	102	97		5.82	1011	173.7113	74.06593	lumens over spec
0.6	27.4	16.44	109	113	101	T2 Exceeds 110C	7.211	1127	156.289	68.55231	lumens over spec
0.7	27.5	19.25	121	124	109	T1/T2 Exceeds 110C	7.99	1175	147.0588	61.03896	lumens over spec

Table 3. Over Powered Engineering Prototype Data

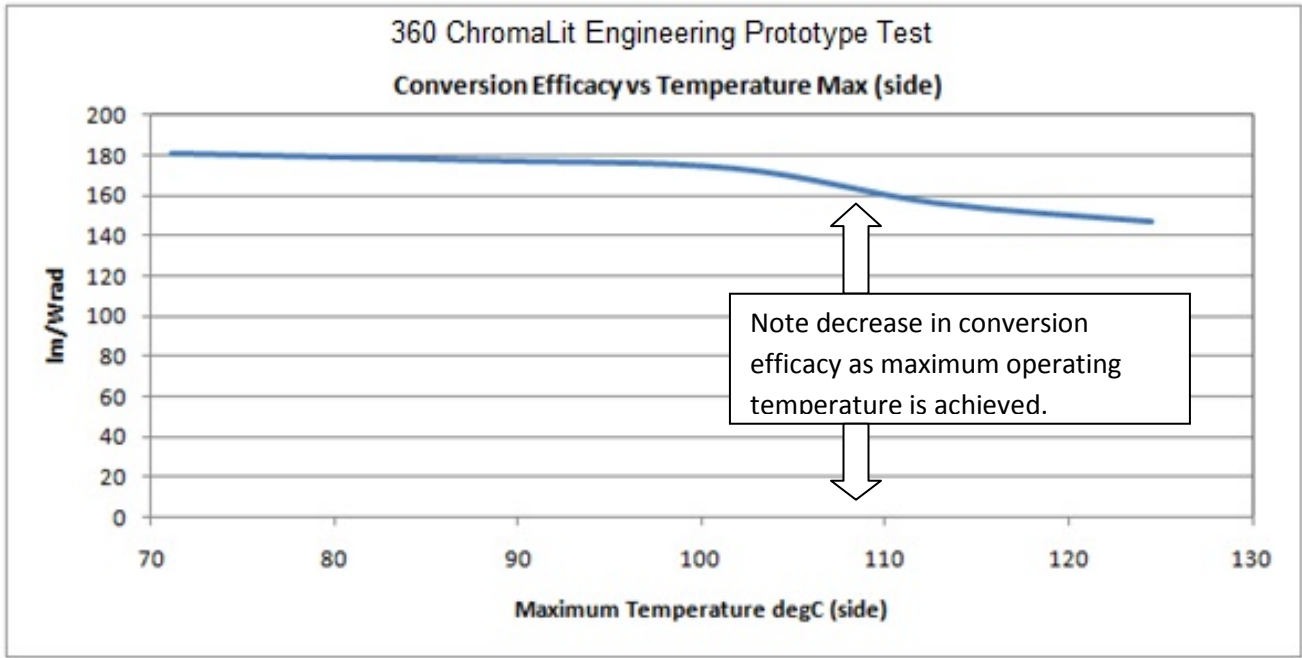


Fig 14 Conversion Efficacy for Over Temp Condition

The graph below shows the input blue watts to the remote phosphor (dotted line, left axis), and the converted lumens (solid line, right axis).

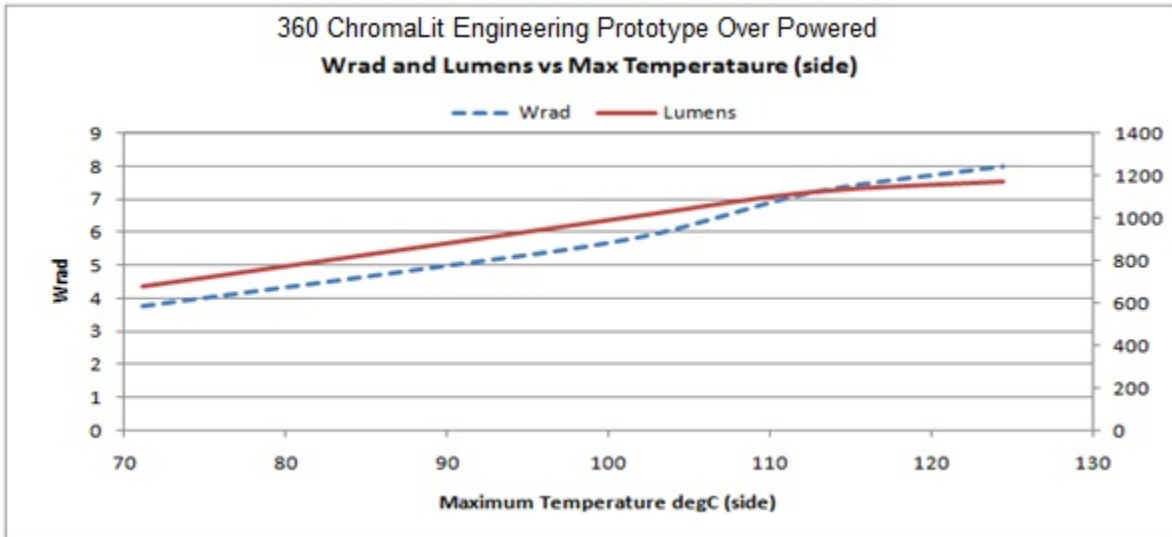


Fig 15. Over Powered Engineering Prototype, Lumens and Wrad

## THERMAL TEST EXAMPLE #2 – Engineering Prototype

Temperature measurements were performed on engineering samples of 2700K source at the test locations shown in the photo below. The ellipse source is powered by ten high flux Rebel ES PR-02 1100 LEDs mounted on a World Class Illumination square MCPCB, which is mounted on a small radial heat sink.

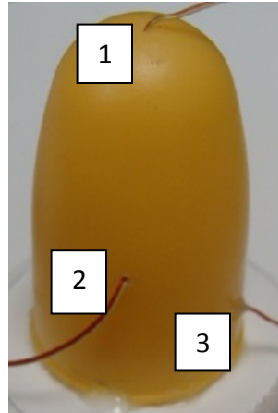


Fig 16. Test Temperature locations.

(Part tested is larger than standard production ellipse part, the inner surface area about 20% larger).

Temperatures measured without glass dome are shown below. A 60-minute stabilization period is allowed for each measurement. The part tested here has an inner surface area about 20% larger than the standard production ELP60.

Vdc(volts)	Idc(amps)	Pdc (watts)	Lumens	T1 (°C)	T2(°C)	T3(°C)
27.2	0.20	5.44	450	37.8	36.7	49.4
27.4	0.30	8.22	655	51.1	52.8	70
27.7	0.40	11.08	829	66.7	64.4	84.4
27.7	0.50	13.95	983	75	72.2	96.7
27.8	0.60	16.68	1108	83.3	79.4	107.8
27.7	0.70	19.38	1203	90	85.6	113.9 (1)

(1) – bottom temperature beyond recommended maximum.

Table 4. Engineering prototype data (without outer envelope).

Additional data is obtained with a glass diffuser envelope in place. No modification to the thermocouple attachment was made between the previous set of data without the outer envelope.

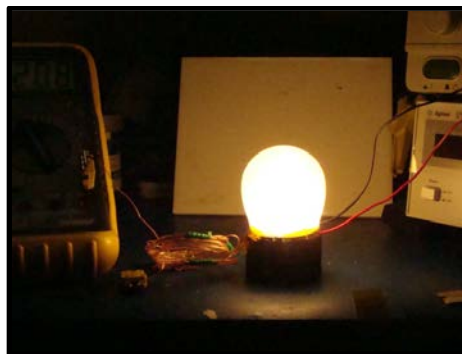


Fig 17. Prototype with diffuse envelope in place.

Vdc(volts)	Idc(amps)	Pdc (watts)	Lumens	T1 (/C)	T2(°C)	T3(°C)
27.1	.20	5.42	408	55.0	51.1	55.0
27.1	.30	8.13	591	71.1	65.0	71.7
27.6	.40	11.04	752	85.6	78.9	83.3
27.6	.50	13.8	889	97.8	91.7	102.2
27.9	.60	16.7	1005	108.3	102.2	114.4
27.6	.70	19.3	1093	116.1	110	125

Table 5. Ellipse Data with Diffuse Envelope

The diffuse envelope in this case raised the temperature as much as 20°C. Smaller volume globes with higher levels of diffusion could raise this difference even further. Therefore, thermal testing should always include the diffuse outer envelope and maximum ambient temperatures to ensure the light source has sufficient thermal design margin.

The following items should be evaluated for their impact on the remote phosphor temperatures:

1. Mounting method (inside press fit to PCB may increase heat load).
2. External envelop, whether diffuse or clear.
3. Any secondary optics.
4. Heat sink and thermal interfaces and thermal interface materials.
5. Ambient temperature and fixture.
6. Power delivery.

## HEAT SINK DESIGN FOR THERMAL MANAGEMENT

As with any LED system design, the LED case temperature will determine to a great extent the life of the product. Worst case scenarios for limited free convection should be considered as well as maximum blue power and converted lumens from the remote phosphor. The remote phosphor source has a low thermal conductivity (about 0.2 W/mK), so the main heat transfer paths for the remote phosphor are by convection and radiation.

The following are some items to keep in mind with regard to proper thermal management.

1. Maximum LED case temperature allowed.
2. Light ray obscuration due to heat sink size near the remote phosphor.
3. Thermal resistance between the LED case and ambient.
4. Thermal conductivity of the heat sink material.
5. Heat sink fin geometry thickness at base, thickness at tip, fin density, and fin height/length.
  - a. Thick fin conducts better.
  - b. Fin spacing too small can limit convection.
  - c. Angled fins can reduce radiation trapping.
  - d. Anodized surfaces better for radiation.
6. Fin orientation verses gravity vector (prefer convective flow along length of fin where possible).
7. Maximum power which must be handled (LED array as well as driver electronics).
8. Interfaces and thermal interface materials. Thermal interface materials (TIMs) are better than air but generally relatively poor in thermal conductivity to metals. Therefore, should use thin layers of TIM. Also want to minimize the number of thermal interfaces which add to thermal resistance.
9. Attachment methods PCB to heat sink, driver to heat sink.

Thermal resistance  $R_T$  is the change in temperature between thermal interfaces divided by the watts transferred.

**Example:** The maximum thermal resistance  $R_{Tmax}$  from the LED case to ambient = (max case temp-ambient temp)/power dissipated. If maximum case temperature is 85°C and ambient is 35°C and the power dissipated is 7 watts, the thermal resistance is  $(85-35)^\circ\text{C}/7 \text{ watts} = 7.14^\circ\text{C}/\text{W}$ .

The total thermal resistance above will be a parallel or series combination of thermal resistances between several thermal interfaces and materials. The PCB to heat sink will be one example. If the thermal resistance of the PCB to heat sink is  $R_{TIM}$ , then the  $R_{Tmax} = R_{TIM} + R_{hs}$ , where  $R_{hs}$  is the thermal resistance of the heat sink to ambient.

The main question is how much surface area is required to convectively cool the product. To derive the minimum cooling surface required start with the thermal resistance equation for the heat sink follows.

$$R_{hs} = (T_{hs} - T_a) / Q$$

$T_{hs}$  = temperature of heat sink  
 $T_a$  = temperature of ambient  
 $Q$  = power dissipated

Rewriting in terms of  $Q = (T_{hs} - T_a) / R_{hs}$

But we also know  $Q = h_c A_s (T_{hs} - T_a)$        $h_c$  = heat transfer coefficient of heat sink.  
 $A_s$  = effective cooling surface of heat sink.

Therefore  $R_{hs} = 1 / h_c A_s$ , which can be written  $A_s = 1 / h_c R_{hs}$

Assuming a heat transfer coefficient of  $5\text{W}/\text{m}^2\text{K}$  and from before  $R_{Tmax}$  (thermal resistance from heat sink to ambient) =  $R_{TIM} + R_{hs}$ , then  $R_{hs} = R_{Tmax} - R_{TIM}$ .  $R_{Tmax}$  was calculated as  $7.14^\circ\text{C}/\text{W}$  and if the thermal interface resistance is  $.2^\circ\text{C}/\text{W}$ , then  $R_{hs} = (7.14 - 0.2)^\circ\text{C}/\text{W} = 6.94^\circ\text{C}/\text{W}$ . So the minimum cooling area is then  $A_s = 1 / h_c R_{hs} = 1 / (5 \text{ W}/\text{m}^2\text{K} \times 6.94^\circ\text{C}/\text{W} \times \text{K}/^\circ\text{C}) = .0288 \text{ m}^2$ . Note that this area is relatively small since the total thermal resistance from the heat sink to ambient is relatively large. To reduce the thermal resistance, the area would be significantly greater.

The calculated watts per surface area ( $7 \text{ watts} / .0288 \text{ m}^2$ ) translates to about  $.06 \text{ watts}/\text{in}^2$ ). A typical value for surface area of heat sink per watt of power is  $10 \text{ in}^2 / \text{watt}$ , which is under the  $16.7 \text{ in}^2 / \text{watt}$  for this example.

Approximate ranges of total thermal resistance for heat sinks based on application and LED power levels are shown below.

Application	Approximate LED Power Range	Approximate range of thermal resistance for heat sink required
Spot Lights	1-15 watts	2.7-9.5 °C/watts
Down Lights	10-60 watts	0.7 to 4.18 °C/watts
Street/High Bay	60-240 watts	0.355 to 0.670 °C/watts

Table 6. Typical Heat Sink Thermal Resistance Ranges

## DISTRIBUTION PATTERNS

Examples of intensity plots for the ChromaLit CAN40 and ELP60 are shown in the figures below. These are bare remote phosphor intensity plots and are found in the data sheet for each source design.

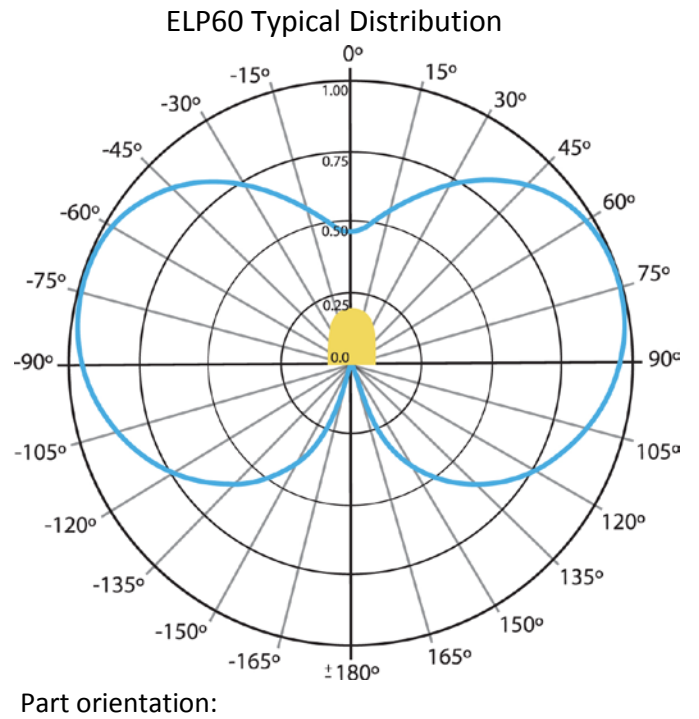
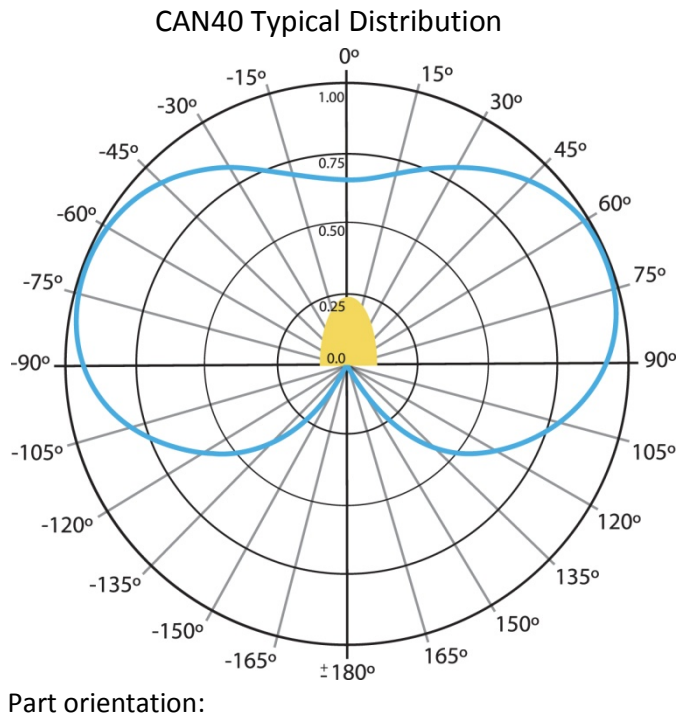


Fig 18. Distributions

The intensity distribution will also be influenced by the diffuse globe surrounding the remote phosphor. The picture below shows an engineering reference bulb that was tested using the elliptical remote phosphor with and without diffuse globes of various size and shape.



Fig 19. Large globe      Test bulb      Small globe

The resulting intensity plots are shown below. Note that the large globe has an extended distribution at the higher angles at the bottom of the bulb (upper portion on plot top right).

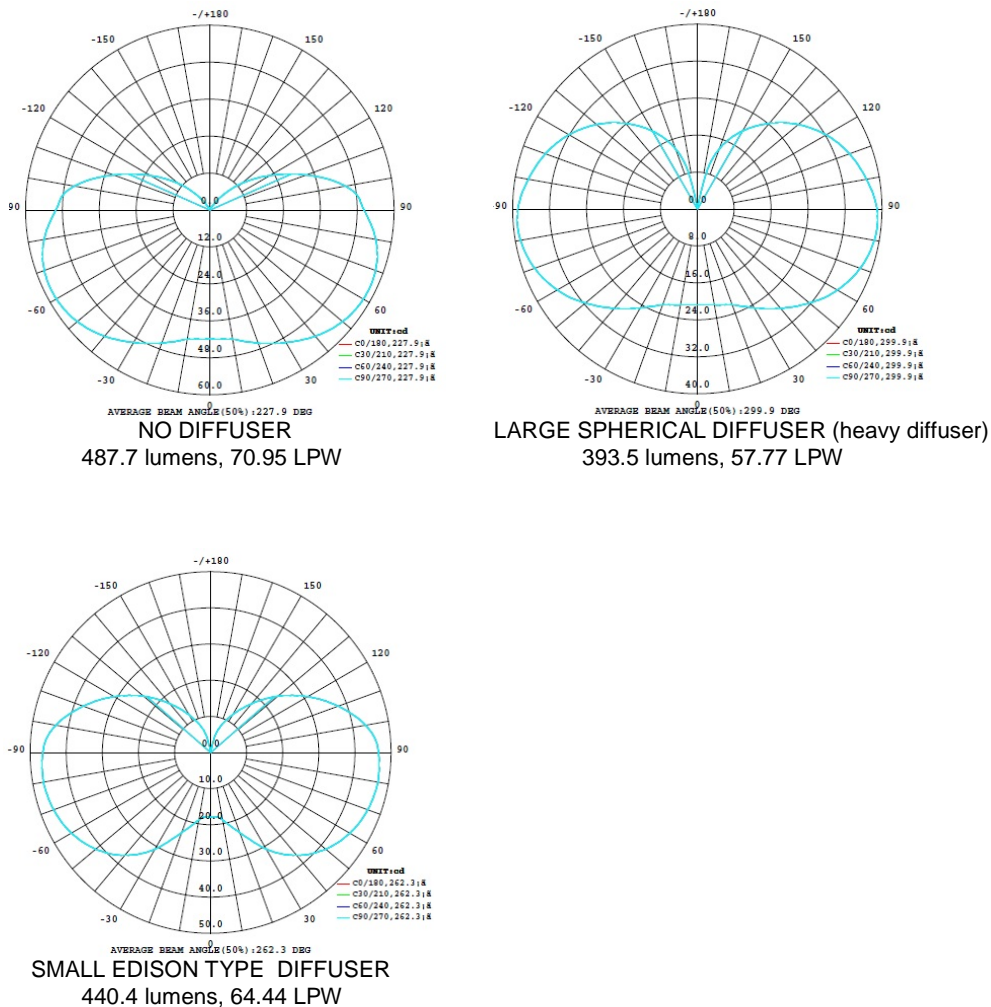


Fig 20. Intensity plots: Note that image is inverted (top of bulb is at bottom of plot).

## ENERGY STAR DISTRIBUTION REQUIREMENTS

The Energy Star requirement for omnidirectional integral bulbs is shown in the figure below. Any diffuse globe used with the ChromaLit remote phosphor can impact this distribution and should be considered early in the design phase.

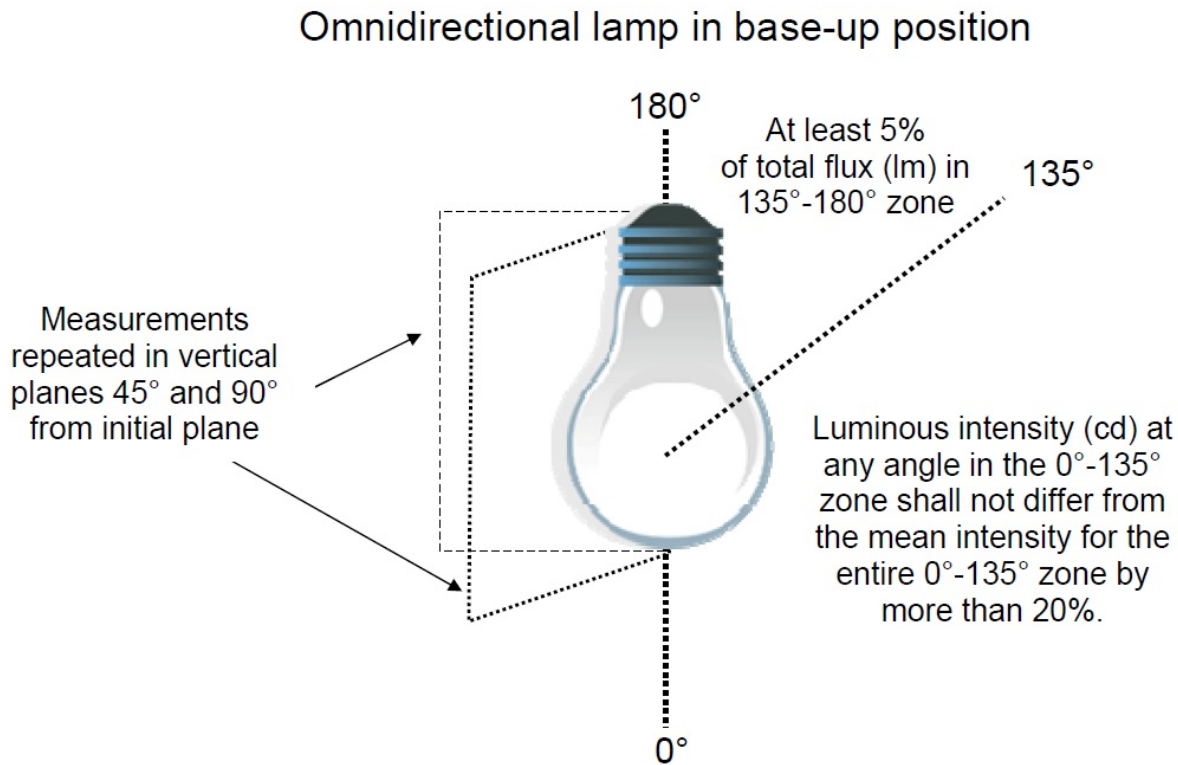


Fig 21. From Energy Star Program Requirements for Integral LED Lamps Partner Commitments  
Appendix B Diagram of Omnidirectional Lamp Zones

Although the distribution of light from the ChromaLit remote phosphor is omnidirectional, it will not automatically satisfy the Energy Star distribution requirements for the Ellipse, Candle, and Dome. Further design details such as diffuse globe design, axially symmetric LED distribution, and heat sink cut-off will need to be considered. For consistent vertical plane distributions an axially symmetric LED distribution is necessary.

## UL CONSIDERATIONS

UL 1993 defines the safety standards for self ballasted lamps such as A19 retrofits. The requirements for the polycarbonates used in such self-ballasted lamps depend on the class rating of the driver as well as the operating environment, whether it is dry, damp, or wet. A wet environment requires a high UV stability rating of f1 and a UL94 flammability rating of 5-VA, 5-VB, or V-0. The ChromaLit is currently undergoing flammability testing per UL94 and UV stability testing. Contact Intematix for the latest information.

## CHROMALIT ATTACHMENT METHOD

The ChromaLit remote phosphor in most cases should be attached as a snap fit to a metal base heat sink that the PCB is mounted to. Since most remote phosphor sources have an external lip or flange, an external mount that captures this is recommended as shown in figure 22 . Some of the remote phosphor sources have two opposing slots at the bottom of the part to allow for compressing the part onto an internal or external diameter. To minimize light loss and prevent color temperature shift at the base, it is recommended to eliminate any external walls around the bottom outside of the remote phosphor. The attachment method below right is recommended. If the outside snap is necessary, the wall height should be just high enough to ensure a strong fit, but low enough to prevent light loss and color shift.

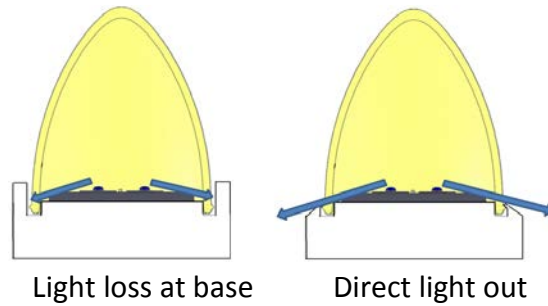
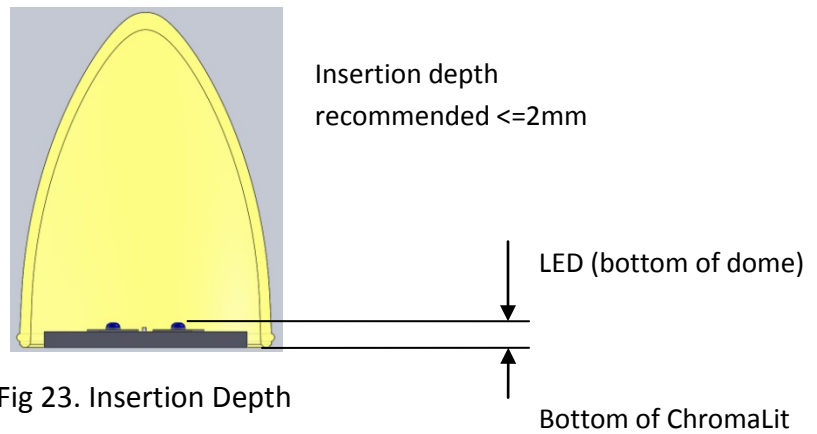


Fig 22. Attachment method and light loss



Above left shows a base with an inside diameter fit to the ChromaLit with a secondary clip or ring that secures the feature on the OD of the ChromaLit. Upper right shows a similar one piece design.

As shown above, it is recommended to design the remote phosphor with an internal fit whenever possible. Large holding structures on the outside of the part can result in significant light loss and/or color shift. Although the inside mount is preferred, an insertion depth of the LEDs should also not be too excessive to preserve uniformity of illumination and far field radiation patterns. Intematix recommends an LED insertion depth of 2mm or less to ensure proper light distribution performance. The insertion depth is measured for all remote phosphor parts from the bottom of the part to the LED emitting surface. A diagram of the insertion depth is shown below.



For additional strength for these mounting methods, a small amount of epoxy can be used on the outside of the remote phosphor. For less stringent requirements epoxy alone may be suitable, however, any mounting scheme should be proven robust with appropriate shock and vibration testing following the expected extremes in operating and storage environmental conditions.

3M DP460 adhesive has been evaluated for ChromaLit -to-metal bond and tested to maximum remote phosphor temperatures without failure. Extreme care should be taken to eliminate any chance of getting epoxy on any of the LEDs and only epoxies with low outgas properties and maximum operating temperatures sufficient for all operating conditions is critical. Cree's XLampXP Soldering and Handling application note is a good reference for materials suitable for use with their LEDs. This reference mentions 3Ms DP190 as suitable for use, however, the maximum temperature is not rated to 110°C. Again, maximum operating temperature for any material must be qualified and for more critical applications, the chosen material should be life tested to ensure long term suitability.

Please refer to the ChromaLit datasheet for detailed product mechanical outline drawings.

## DESIGNING THE MIXING CHAMBER

The mixing chamber for the remote 3D phosphor is now simply a reflective planar surface surrounding the LEDs rather than a volume between the LEDs and remote phosphor. As with the 2D case, it is critical that the material be a highly reflective diffuse material with operating temperature rating acceptable for the application. See Intematix Application Note for Mixing Chambers.

The material should have minimal thickness. To minimize blue light loss at the LED aperture, 0.5mm is best. The material can be hard formed and screwed into place or adhesive backed, but the adhesive must be suitable for the high operating temperatures and compatible with the LED encapsulants. W.L. Gore Associates DRP material provides a thin .5mm thick high temperature material that provides a nice conformal layer and is available in a high temperature adhesive backed version for easy installation. This is presently a high-end product only for applications that can absorb a high raw material cost. Genesis, White Optics, and Furukawa also have available lower cost materials with similar characteristics. CerFlex molds ceramic materials to shape with extremely high operating temperatures and reasonable cost structure.

Examples of mixing chamber materials surrounding various LED arrays are shown below. A thin material will minimize the trapping of blue power around the LED aperture. Coverage of the complete LED device is preferable, but may be sacrificed for best collection of LED blue power at the aperture edge.



Fig 24. Mixing chamber material around LED arrays.

Note that even a small screw as shown bottom left can impact performance. It is recommended to use a flat head screw whenever possible to reduce light obstruction. It is also recommended to cover the screw with highly reflective material. For the case below, an improvement of 5 percent in total luminous flux was obtained after covering the stainless steel screw with diffuse reflective material.



Fig 25. Flat head screw recommended.



Fig 26. Reflective material on screw = 5% gain.

Typical increase in efficacy is about 6% with highly reflective material compared to a bare PCB for a 20mm diameter board populated with 6 LEDs. Even larger gains are expected for non-white PCBs. A higher than expected color temperature may be due to a low reflectivity material that does not efficiently recycle the down converted rays hitting it's surface.

The mechanical mounting of the LED PCB as well as wire gauge and routing should be considered in combination with the selection of reflective material type and thickness. Large wire gauge will result in considerable difficulty in achieving a flat reflective surface as will routing wires across the PCB. The pictures below show the difference between selection of insulated 22-gauge wire and 28-gauge wire. The 28-gauge wire can provide a relatively flat solder locations and the wires can be routed more efficiently through a PCB slot or straight through the board. Although smallest wire gauge is desired, the proper wire gauge must be determined for the operating current required.



Fig 27. 10 LED PCB 22 gauge

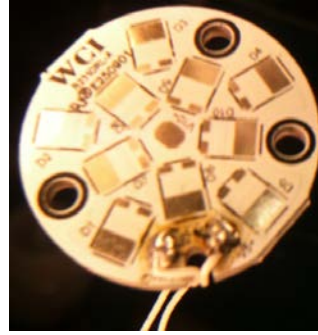


Fig 28. Same 10LED board 28 gauge

## DRIVER CONSIDERATIONS

The first step for any design is to evaluate how much power must be delivered to achieve the required lumen output. This can be derived from the remote phosphor conversion efficacy x blue watts = lumens. This lumens value will need to be higher to compensate for AC to DC driver loss, diffuse cover glass loss, LED thermal losses, and any losses in light due to absorption around the LED (mixing chamber losses and heat sink obscuration). If dimming is necessary, additional AC to DC power losses must be accounted for.

LEDs work best with constant current. For series connected LEDs driving the same current through all devices will be more straightforward. The power supply must of course be able to deliver a voltage of at least the sum of the maximum forward voltage of each device in a string. Since forward voltage may have relatively large tolerances for the chosen LED and the forward voltage depends on operating case temperatures, a driver capable of large voltage range with precise current delivery is necessary. Power factor correction is a common necessary requirement as well as acceptance of universal line voltage (80VAC to 277VAC).

Typical Specifications:

Input Voltage Min, Typ, Max

Output voltage Min, Typ, Max

Output Current/Power

Efficiency full load (verses input voltage)

Power factor

Conducted EMI – meets CISPR 15B/EN550 15B

Harmonics EN61000-3-2-class D

Maximum ambient temperature (typical free convection/sea level)

### Power Factor

The power factor of an AC electric power system is a dimensionless number between 0 and 1, defined as the ratio of the real power flowing to the load to the apparent power in the circuit. It is sometimes, but not always, the cosine of the angle between the voltage and current.

The definition of power factor (PF) is:

$$PF = \frac{\text{real power (capacity to perform real work in a particular time)}}{\text{apparent power (VA)}}$$

Due to energy stored in the load and returned to the source, or due to a non-linear load that distorts the wave shape of the current drawn from the source, the apparent power will be greater than the real power.

In an electric power system, a load with a low power factor draws more current than a load with a high power factor for the same amount of real power. The higher currents produce higher  $I^2R$  losses so larger component selection is necessary. Requirements for high power factor systems, with lower power loss is therefore desired.

**Driver Topologies:**

<b>Power Supply Topology</b>	<b>Benefits</b>	<b>Drawbacks</b>	<b>Comments</b>
Linear regulator	Low part count, low EMI	Low power factor since current is drawn from the mains at the voltage peaks. Runs hotter.	
Step-down, Buck regulator	High efficiency	Electromagnetic interference produced due to the current being switched on and off sharply. Appropriate filtering is required to meet conducted and radiated emission specs.	When input voltage exceeds LED(s) total voltage
Step-up, Boost regulator	Simplest design for >350ma and variable voltage.	“	When minimum forward voltage always exceeds input voltage.
Buck-boost, SEPIC, Cuk, flyback, Vn referenced buck-boost (floating buck boost)		“	Input voltage overlaps LED voltage range

Table 7. Power Supply Topology

Several sources of LED drivers:

- Marvell
- National
- Mean Well
- Roal
- Inventronics
- Fairchild Semiconductor
- STMicroelectronics
- Dialight
- Harvard Engineering PLC
- Eptronics
- Maxim
- Power Integrations
- Exclara

## CHROMALIT DEMO KIT

Intematix has a demonstration kit for the ChromaLit Can40, which uses a 6-LED Philips Rebel PCB, metal body for attachment to a heat sink for long term operation as well as a battery pack for quick demonstration purposes.

The pictures below show the demo kit hardware. Obtain a copy of the demo kit reference design document for details on performance data using the demo kit. Contact Intematix to obtain technical document, pricing and delivery information for these reference design kits.

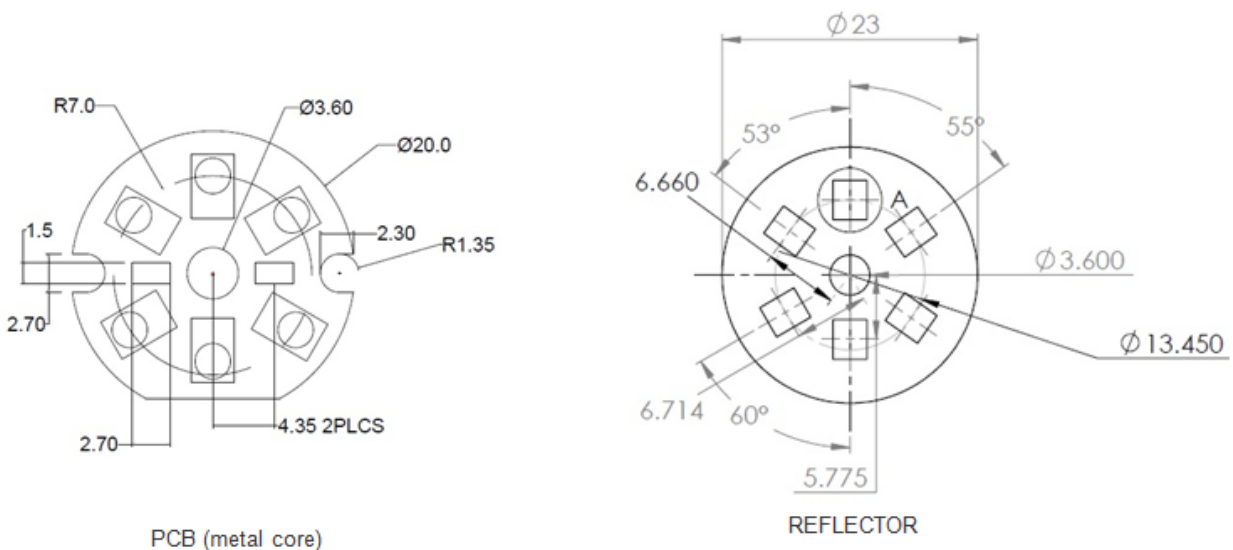


Fig 29. Reference kit 6LED.



Fig 30. Reference kit assembly.

This reference kit does require a heat sink for prolonged operation. A threaded #4-40 screw is located on the center back side of the PCB base.



Dimension in millimeters (not to scale)